

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Hung T. Nguyen, et al.

Serial No.: 10/007,498

Filed: 11/13/2001

For: PIPELINED MULTIPLY-ACCUMULATE UNIT AND OUT-OF-ORDER COMPLETION LOGIC FOR A SUPERSCALAR DIGITAL SIGNAL PROCESSOR AND METHOD OF OPERATION THEREOF

Group No.: 2181

Examiner: Tonia L. Meonske Confirmation No.: 2278

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed with the United States Patent and Trademark Office on

January 15, 2007 (Date)

Karen Vertz
(Printed or typed name of person signing the certificate)

Sir:

Karen Vertz
(Signature of the person signing the certificate)

ASSERTION OF SMALL ENTITY STATUS (37 CFR § 1.27(c)(1))

Applicant hereby states that applicant is a small entity and that status as a small entity is asserted for this

application

patent.

Respectfully submitted,



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